



# LC6006

#### LDO mode OVP with Integrated P-MOSFET

#### DESCRIPTION

The LC6006 is Li+ charger IC with integrated P-MOSFET. The device is fabricated with advanced CMOS technology to achieve maintaining low static power dissipation over a very broad VCC operating range.

The LC6006 integrates a P-MOSFET and Schottky diode which is normally a discrete device employed for conventional battery charging design of mobile phone system. In addition to that, LC6006 works like a LDO mode to keep CHRIN voltage stable when ACIN goes high. And thus it will not trigger the CHRIN pin over-voltage protection when ACIN voltage increased to as high as 15V.

The LC6006 provides complete Li+ charger protections and saves the external MOSFET and Schottky diode for the charger of cell phone's PMIC. It is available in a DFN2x2-8L package.

The above features and small package make the LC6006 an ideal part for cell phones applications.

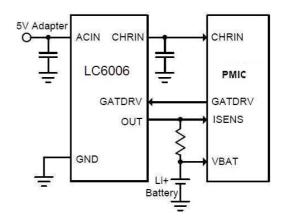
#### **FEATURES**

- A Built-In P-MOSFET
- LDO mode makes CHRIN voltage stable around 5.5V
- Range of operation input voltage: Max 15V
- Charging current up to 1A
- Environment Temperature: -20°C~85°C

#### **APPLICATIONS**

• Cell phone and other portable device

#### **APPLICATION CIRCUIT**



### **ORDERING INFORMATION / PIN CONFIGURATION / MARKING**

LC6006CKBTR DFN2x2-8L	LC6006CB6TR SOT23-6L	Top Marking		
ACIN 1 ACIN 2 GND 3 NC 4 ACIN 2 BP BP B B B C B C B C B C B C B C C C C	OUT1 CHRIN2 GATDRV3 CHRIN2 4 N C	OB <u>YW</u>		

<u>YW</u> means the year and week parts being manufactured, subjected to change. OB is the code of the product; it will not be changed on any part.

1

## ABSOLUTE MAXIMUM RATING (Note1)

Parameter	Symbol	Rate	Unit	
ACIN Input Voltage (ACIN to GND)	V <sub>ACIN</sub>	-0.3~15	V	
CHRIN to GND Voltage	V <sub>CHRIN</sub>	-0.3~6	V	
GATDRV to GND Voltage	V <sub>GATDRV</sub>	-0.3~ V <sub>CHRIN</sub>	V	
OUT to GND Voltage	V <sub>OUT</sub>	-0.3~6	V	
Output power limit, lout x (V <sub>ACIN</sub> -V <sub>OUT</sub> )	P <sub>D</sub>	0.75	W	
Maximum Junction Temperature	ΤJ	150		
Storage Temperature	T <sub>STG</sub>	-40 to 150	°C	
Maximum Lead Soldering Temperature, 10 Seconds	T <sub>SDR</sub>	260		

<sup>Note 1</sup>: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired. Exposure to absolute maximum rating conditions for extended periods may destroy the device.

#### THERMAL RESISTANCE RATING

Parameter	Device	Symbol	Typical	Unit
Junction-to-Ambient Resistance in Free Air <sup>(Note2)</sup>	DFN2x2-8	$\theta_{JA}$	80	°C /W
Junction-to-Ambient Resistance in Free Air	SOT23-6	$\theta_{JA}$	235	°C /W

Note 2:  $\theta_{JA}$  is measured with the component mounted on a high effective thermal conductivity test board in free air. The exposed pad of DFN2x2-8 is soldered directly on the PCB.

#### THERMAL CONSIDERATION

Even though LC6006 can handle charge current larger than 1A, it is also limited by the power dissipation of the package DFN2x2-8L. The DFN2x2 package has a thermal pad exposed, and it should be tightly soldered to bottom PCB with a large coil area to dissipate the heat. In general, to have the LC6006 to work under a safe condition, one should take DFN2x2 power limit as 0.75W, and if the dropout voltage is 1.5V, one is suggested to set the charging current to be less than 500mA.

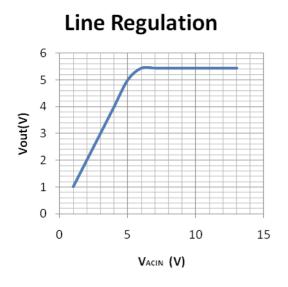
#### **RECOMMENDED OPERATION CONDITIONS**

Symbol	Parameter	Range	Unit
V <sub>ACIN</sub>	ACIN Input Voltage (ACIN to GND)	4.5~10	V
lout	Ooutput Current	0~700	mA
T <sub>A</sub>	Ambient Temperature	-40~85	°C
TJ	Junction Temperature	-40~125	°C

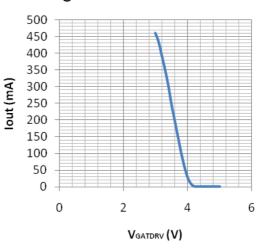
#### **ELECTRICAL CHARACTERISTICS**

						Tj=25°C
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Vth	Threshold Voltage	Ids=-1uA, Vds=Vgs	-1.0	-0.7	-0.4	V
V <sub>CHRIN2</sub>	CHRIN Voltage	V <sub>IN</sub> = 6.0 V, I <sub>CHRIN</sub> =50mA	5.0	5.5	6.0	V
IDss1	off-state leakage	$V_{OUT}=0$ , $V_{IN}=10V$ , $V_{GATDRV}=V_{CHRIN1}$	-	-	1	uA
IDss2	reverse block leakage	$V_{OUT}=5V, V_{IN}=0, V_{GATDRV}=V_{CHRIN1}=0V$		2	5	uA
Idson	On –state drain current	V <sub>IN</sub> =5V, V <sub>OUT</sub> =4V, V <sub>GATDRV</sub> =1V	0.9	1.2	1.5	А
Rdson	Vds/Idson	Vs=5V, Vg=1V, Vd=4V	0.5	0.75	1	ohm

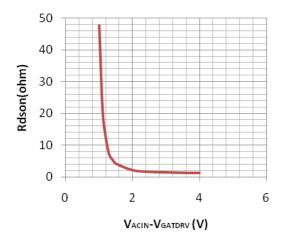
#### TYPICAL PERFORMANCE CHARACTERISTICST=25°C unless specified.



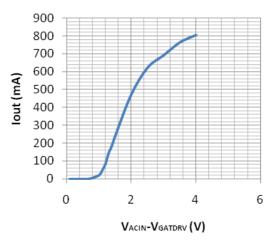
#### Charger Current Vs VGATDRV



**On Resistance** 



**Transfer Character** 



900 Vgs=1.5v 800 Vgs=2v 700 Vgs=3v 600 lout(mA) Vgs=4v 500 400 300 200 100 0 0 2 6 4 VACIN-VOUT (V)

**Output Character** 

#### **PACKAGE OUTLINE** Package DFN2x2-8 Devices per reel 3000 Unit mm Package specification: D2-Į., ш ī A3-T E2 ſī A F h D ē PIN 1 DOT TOP VIEW SIDE VIEW BY MARKING BOTTOM VIEW PIN #1 **IDENTIFICATION** COMMON DIMENSIONS(MM) W: VERY VERY THIN PKG REF NOM MIN МАХ 0.70 0.80A 75 n A1 0 00 0.05 A3 REF 0 D 1.9500 2 05 95.05 00 Ь 0.18 23 0.30 0 0.2535 0.45 05 30 D 20 1 E2 0.45 0.600.70 50 BSC e C SOT23-6 Devices per reel 3000 Package Unit mm Package specification: 1.2 COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER) 4-01-SYMBOL MIN NOM MAX 1.25 A A1 0 A4 02 1.20 A2 1.10 1.00 0.60 0.65 A3 0.36 0.50 b b1 E1 0.38 0.36 0.45 0.14 0.20 C 0.15 0.14 c1 2.826 2.60 1.526 2.926 2.80 3.026 D 3.00 1.726 1.00 2.00 1 1.626 E $\mathbb{A}$ 0.90 0.95 e 1.90 0.45 1.80 el **PIN #1** 0.60 B P 0.59RE L1 0.25850 2 A 0.10 R WITH PLATING 0.10 A R1 0.20 5 A 14 θ BASE METAL SECTION B-B

Leadchip Semiconductor tries its best effort to keep any content of this document correct at its best knowledge, but it assumes no responsibility for any errors which may appear in this document, reserves the right to change devices or specifications detailed herein at any time without notice in advance. No licenses to patents or other intellectual property of Leadchip Semiconductor are granted in connection with the sale of its products, expressly or by implications.

